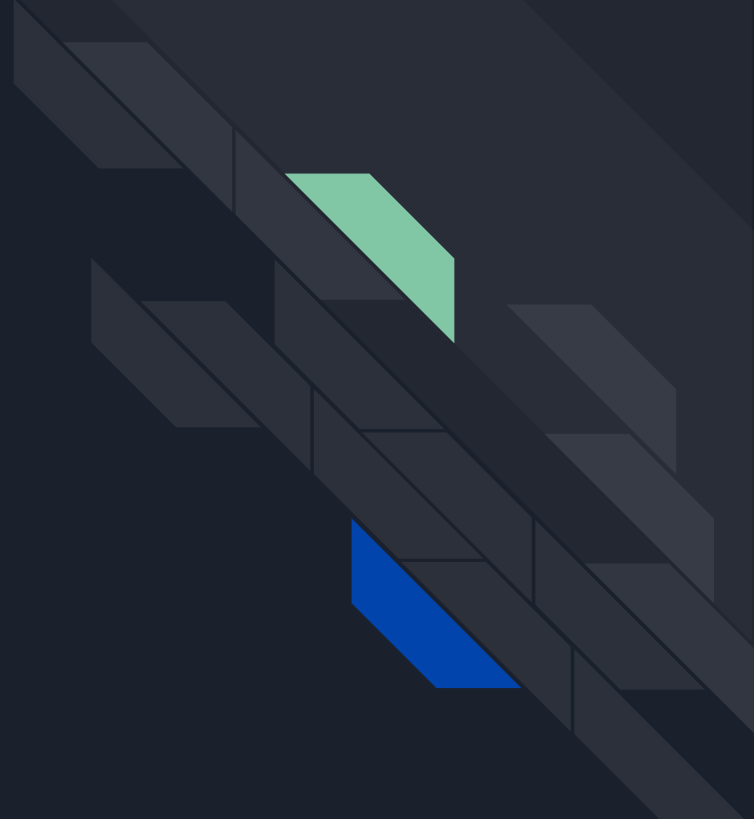


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Tales of C, the 6502 and the BBC

With added python

Because python is awesome

I'm not

From the 80's



BACK
TO
THE 80s

The text is rendered in a bold, italicized, sans-serif font. The word 'BACK' is on the top line, 'TO' is on the second line, and 'THE 80s' is on the third line. The letters have a vertical gradient from red at the top to yellow at the bottom. A light blue outline surrounds each letter. The 'K' in 'BACK' is stylized with a long, multi-segmented arrow pointing to the right, also following the red-to-yellow gradient.



It all starts with



With 80's appropriate logo



The computer literacy project

BBC

CONTINUING EDUCATION TELEVISION

Computer Literacy Project

The Acorn BBC Micro



6502 assembly

ADC...add with carry	CMP	compare (with accumulator)	PLP	pull processor status (SR)
AND...and (with accumulator)	CPX	compare with X	ROL	rotate left
ASL...arithmetic shift left	CPY	compare with Y	ROR	rotate right
BCC...branch on carry clear	DEC	decrement	RTI	return from interrupt
BCS...branch on carry set	DEX	decrement X	RTS	return from subroutine
BEQ...branch on equal (zero set)	DEY	decrement Y	SBC	subtract with carry
BIT...bit test	EOR	exclusive or (with accumulator)	SEC	set carry
BMI...branch on minus (negative set)	INC	increment	SED	set decimal
BNE...branch on not equal (zero clear)	INX	increment X	SEI	set interrupt disable
BPL...branch on plus (negative clear)	INY	increment Y	STA	store accumulator
BRK...interrupt	JMP	jump	STX	store X
BVC...branch on overflow clear	JSR	jump subroutine	STY	store Y
BVS...branch on overflow set	LDA	load accumulator	TAX	transfer accumulator to X
CLC...clear carry	LDY	load X	TAY	transfer accumulator to Y
CLD...clear decimal	LDY	load Y	TSX	transfer stack pointer to X
CLI...clear interrupt disable	LSR	logical shift right	TXA	transfer X to accumulator
CLV...clear overflow	NOP	no operation	TXS	transfer X to stack pointer
	ORA	or with accumulator	TYA	transfer Y to accumulator
	PHA	push accumulator			
	PHP	push processor status (SR)			
	PLA	pull accumulator			



An if (passing)

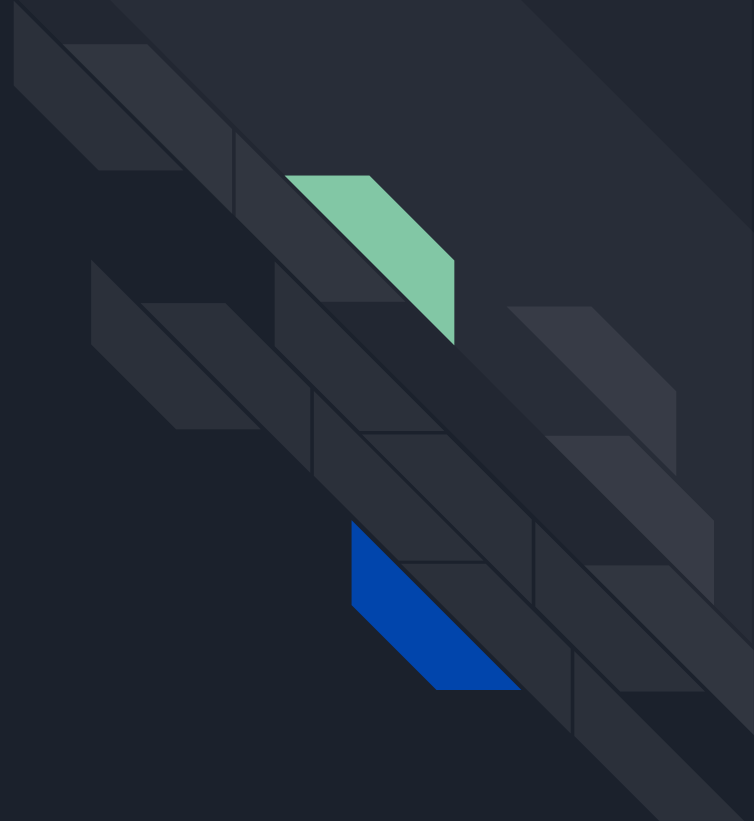
- ➡ LDA #\$42 (Load accumulator)
- ➡ CMP #\$44 (Compare value in accumulator with absolute value)
- ➡ BMI thing (Branch on minus)
- ➡ JMP elsething
- ➡ .thing *** *Do thing* ***
- ➡ JMP endthing
- ➡ .elsething *** *Do other thing* ***
- ➡ .endthing
- ➡ *** *Rest of program* ***

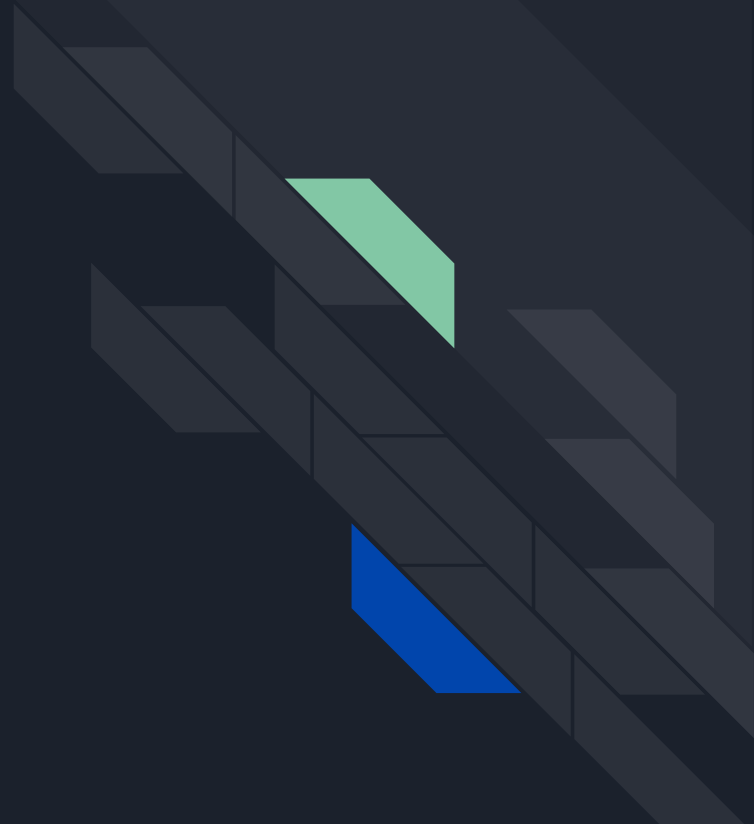


An if (failing)

→ LDA #\$42 (Load accumulator)
→ CMP #\$40 (Compare value in accumulator with absolute value)
→ BMI thing (Branch on minus)
→ JMP elsething
 .thing *** *Do thing* ***
 JMP endthing
→ .elsething *** *Do other thing* ***
→ .endthing
→ *** *Rest of program* ***

I can
compilez





How does one compile?



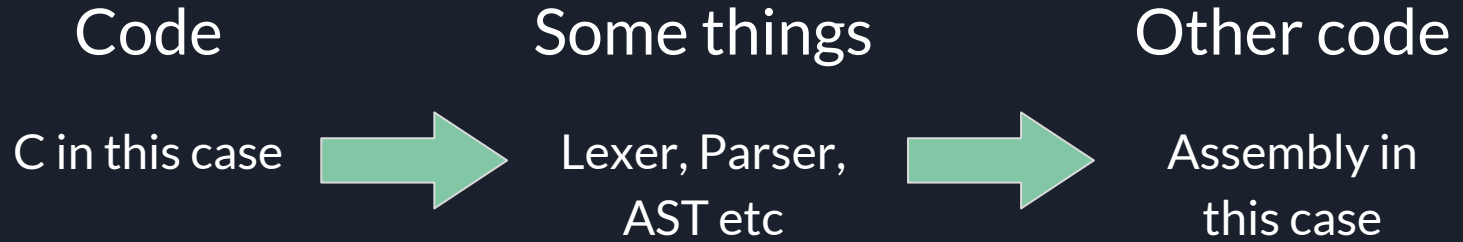


What does a compiler do





What does a compiler do



Step 1

A lexer





A lexwhaaaat?

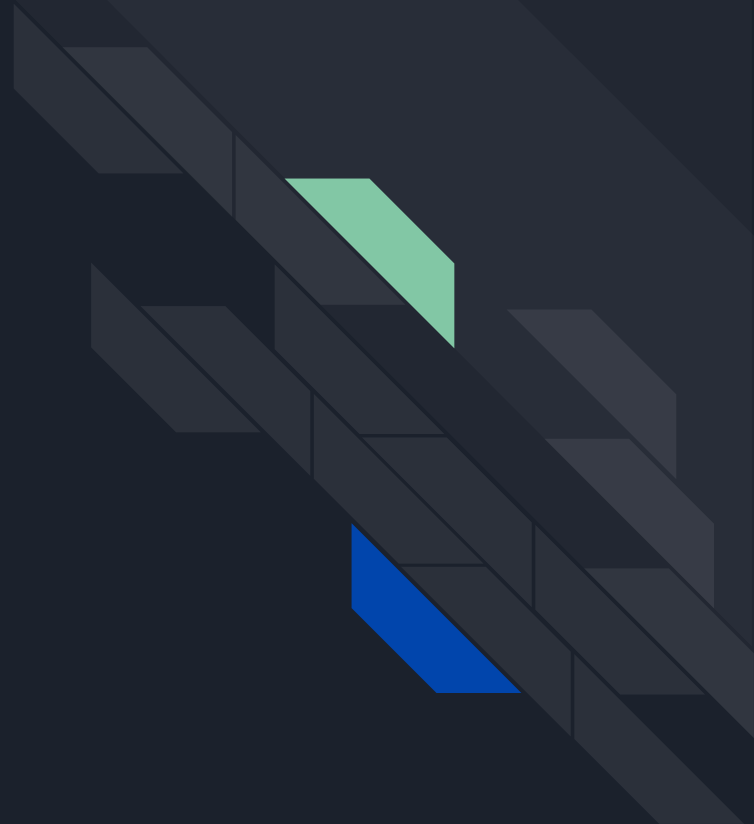
```
int main() {  
    return 1 + 2;  
}
```



```
Token(INT, 'int')  
Token(ID, 'main')  
Token(LBRACK, '(')  
Token(RBRACK, ')')  
Token(LBRACE, '{')  
Token(RETURN, 'return')  
Token(INT, 1)  
Token(PLUS, '+')  
Token(INT, 2)  
Token(SEMI, ';')  
Token(RBRACE, '}')
```

Step 2

A parser and AST



Autonomous Sugary Topiary?

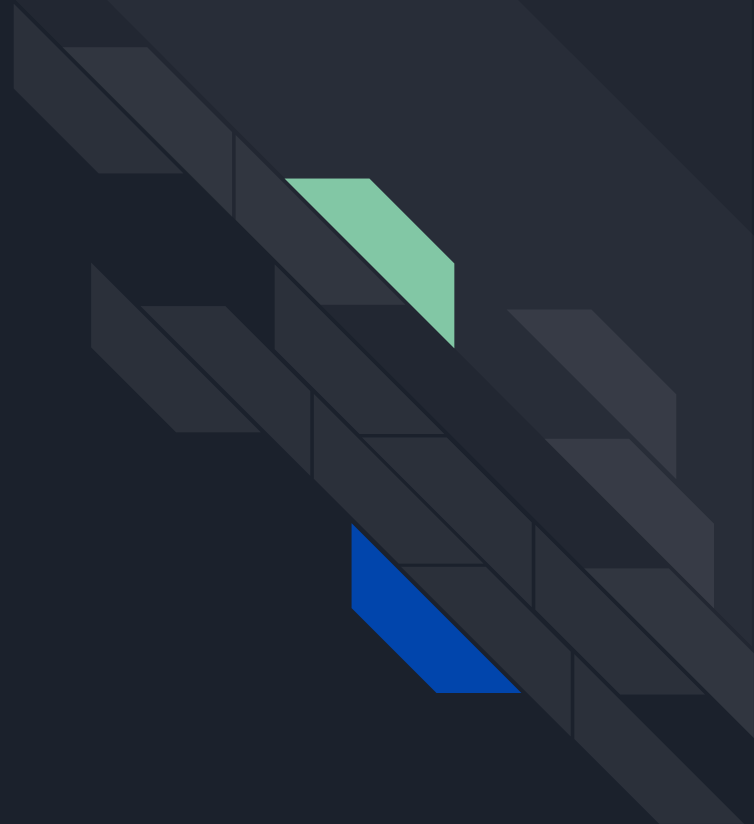
```
Token(INT, `int`)  
Token(ID, `main`)  
Token(LBRACK, `(`)  
Token(RBRACK, `)`)  
Token(LBRACE, `{`)  
Token(RETURN, `return`)  
Token(INT, 1)  
Token(PLUS, `+`)  
Token(INT, 2)  
Token(SEMI, `;`)  
Token(RBRACE, `}`)
```




```
TranslationUnit  
Function<main:<IntegerCType(2:True)>>()  
Compound  
Return  
Plus  
Number<1>  
Number<2>
```

Step 3

Assembly, finally



Compiled Assembly (No pun this time)

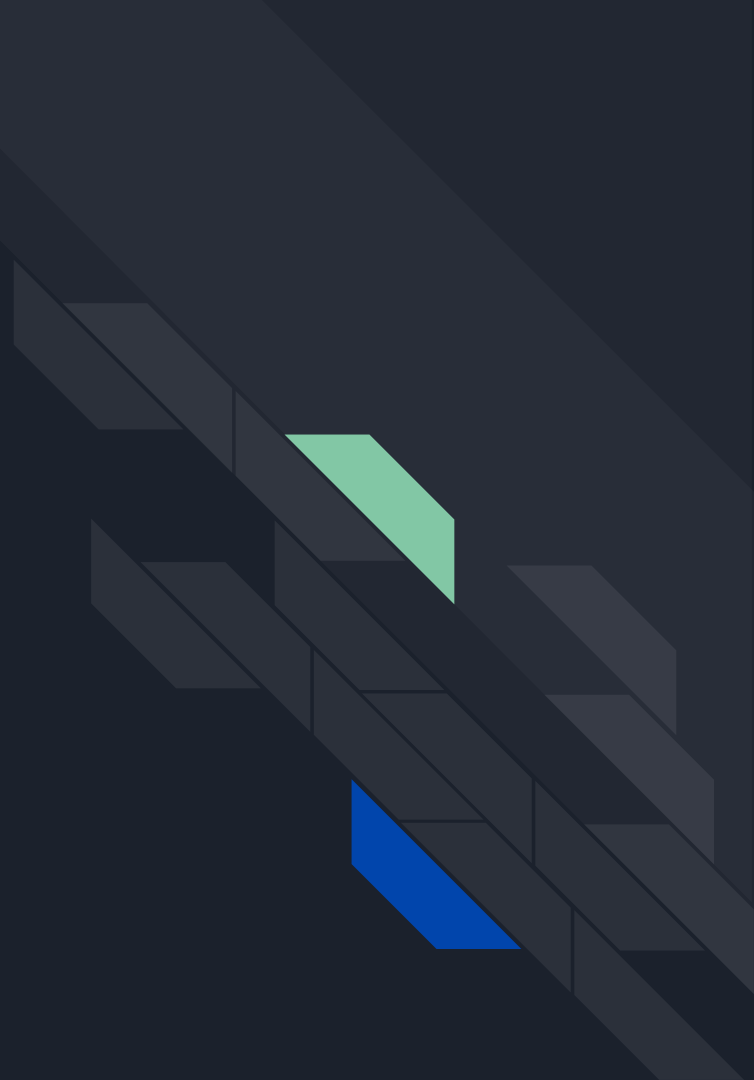


NEW	23 TAY	47 LDA (&8E),Y	71 JSR _setup_global	95 CALL _start
0 FOR opt%=0 TO 2 STEP 2	24 LDY #03	48 INC &8E	72	96 RH=&70
1 P%=&E00	25 LDA (&8E),Y	49 BEQ _bbcc_pulla_1	73 \ CallFunction	97 RL=&71
2 [26 TAX	50 RTS	74 JSR __main	98 PRINT ~?RH,~?RL
3 OPT opt%	27 LDY #01	51 ._bbcc_pulla_1	75	
4 ._putchar	28 LDA (&8E),Y	52 INC &8F	76 \ Return	
5 LDY #01	29 JSR &FFF4	53 RTS	77 RTS	
6 LDA (&8E),Y	30 STX &71	54	78	
7 JSR &FFEE	31 LDA #00	55 \ Label	79 \ Function	
8 STA &71	32 STA &70	56 ._setup_global	80 __main	
9 LDA #00	33 RTS	57	81	
10 STA &70	34	58 \ Return	82 \ Add	
11 RTS	35 \ Routines	59 RTS	83 CLC	
12	36 ._bbcc_pusha PHA	60	84 LDA #&01	
13 ._getchar	37 LDA &8E	61 \ Function	85 ADC #&02	
14 JSR &FFE0	38 BNE _bbcc_pusha_1	62 ._start	86 STA &71	
15 STA &71	39 DEC &8F	63	87 LDA #&00	
16 LDA #00	40 ._bbcc_pusha_1	64 \ Set	88 ADC #&00	
17 STA &70	41 DEC &8E	65 LDA #&00	89 STA &70	
18 RTS	42 PLA	66 STA &8E	90	
19	43 LDY #00	67 LDA #&18	91 \ Return	
20 ._osbyte	44 STA (&8E),Y	68 STA &8F	92 RTS	
21 LDY #05	45 RTS	69	93]	
22 LDA (&8E),Y	46 ._bbcc_pulla LDY #0	70 \ JmpSub	94 NEXT opt%	

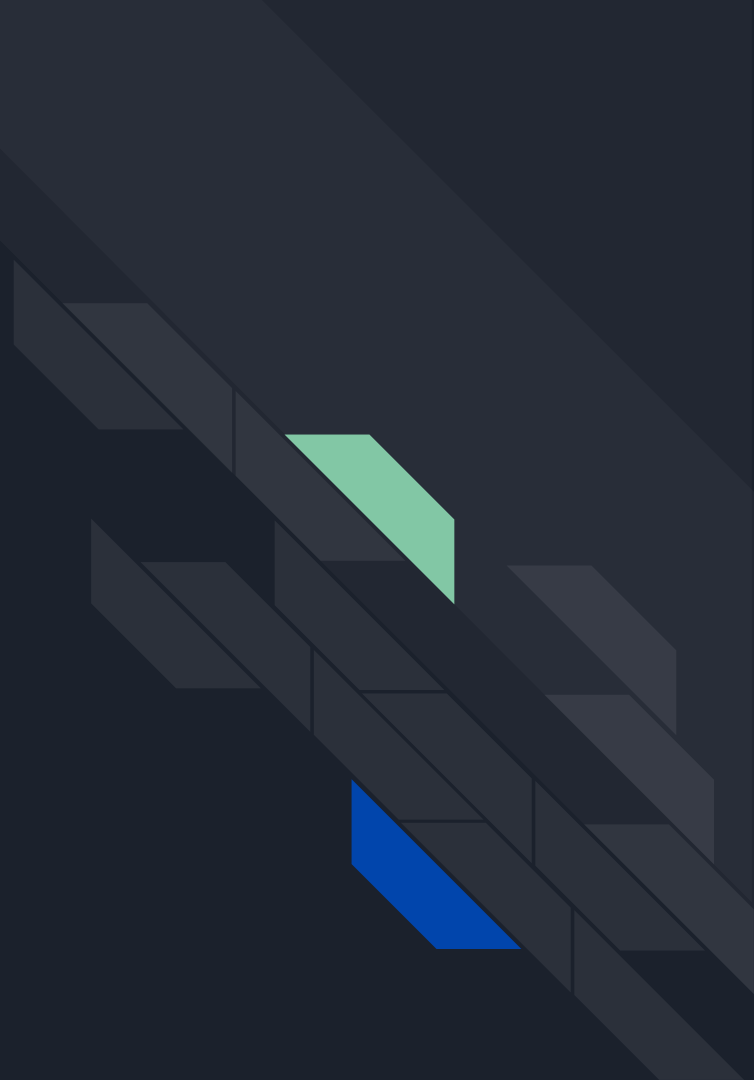
Demo time

~~Or it be if I could SSH into a FBC
Micro~~

Scrap that! Yes I can!

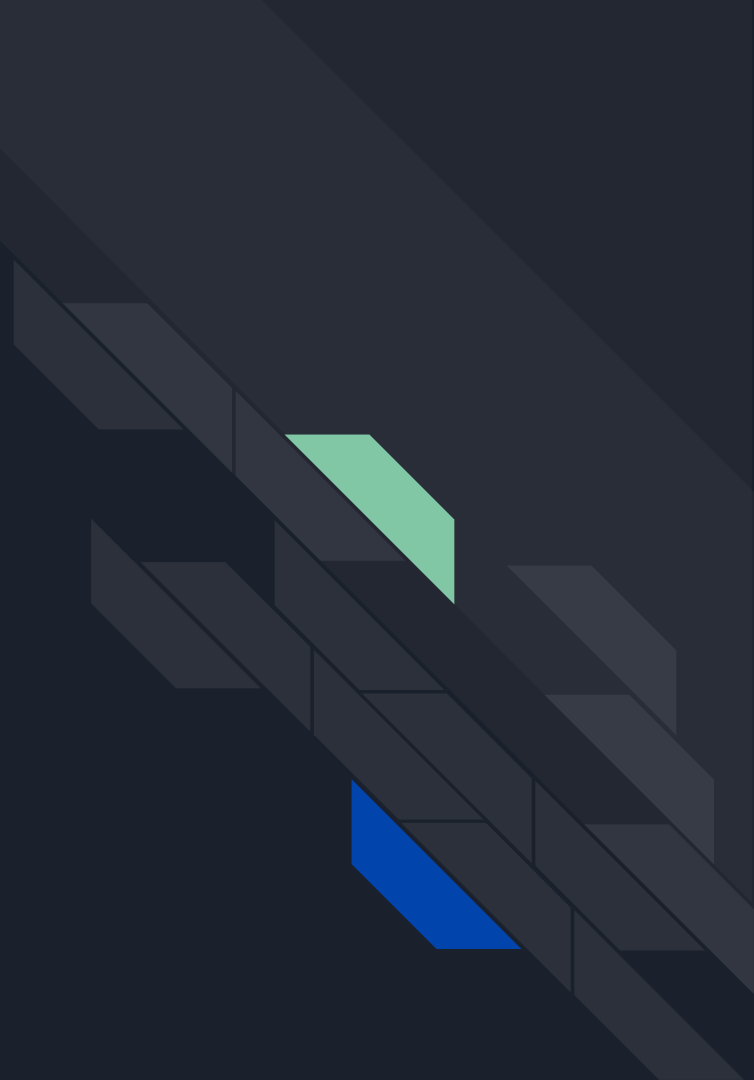


SSH into a what?

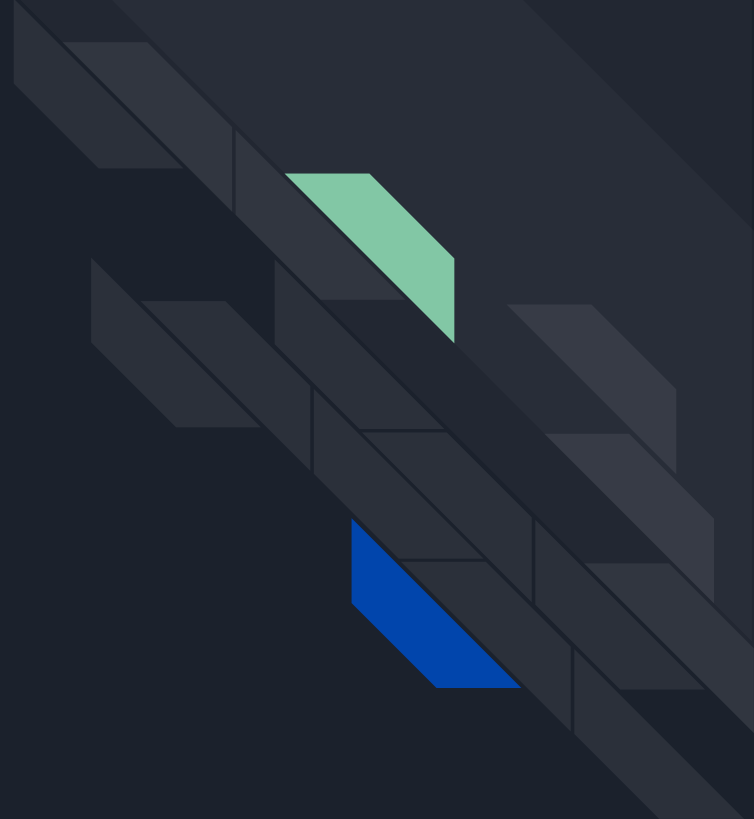


One final thing

How do variables work?



A Symbol Table





A diagram (anyone spot a theme)

ID	Type	Location
a	INT	0x2FA
b	CHAR	0x2FC
c	INT[4]	0x2FD
d	CHAR[3]	0x305
e	INT	0x308



Diagram #I've lost count

ID	Type	Location
a	INT	0x2FA
b	CHAR	0x2FC

Global Scope

Local Scope 1

c	INT[4]	SP+0
d	CHAR[3]	SP+8

Local Scope 2

e	INT	SP+0
---	-----	------

Thank you
I've been Benjamin
Misell
Questions?

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