How To Write A Testable State Machine

Matthew Jones ACCU Conference, 26th April 2012

State Machines

- Describe your 'nightmare' state machine
- 1000 line file
- Mother of all switch() statements
- 10s of line per case
- Nested switch()es
- Freely calling other code to implement the state
- What is the cyclometric complexity?
- Often an entire thread's code in one place
- Classic testing problem

```
Writing a Testable State Machine
```

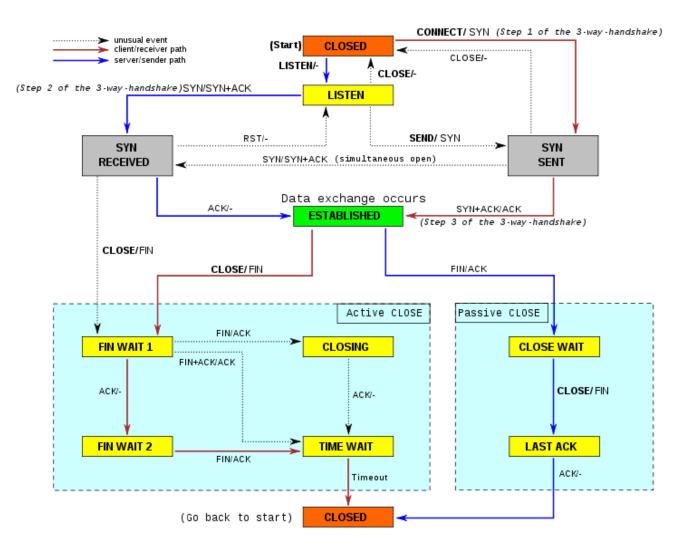
```
switch (TK_CUR_TYP(peb-stke)) {
Case TK_TT_STAR:
    if (peb->flags & XP_PL_INSTANCEID) {
         invalid instanceid error(ocb):
         return ERR_NCX_INVALID_INSTANCEID;
    break:
Case TK_TT_NCNAME_STAR:
     if (peb->flags & XP_PL_INSTANCEID) {
         invalid_instanceid_error(pcb);
         return ERR NCX INVALID INSTANCEID:
      * match all nodes in the namespace w/ specified prefix */
    if (locb-otke-ocur-onsid) {
         res - check_gname_prefix(pcb,
                                      TK_CUR_VAL(peb->tke),
                                      xml_strlen(TK_CUM_VAL(peb-stke));
                                     &peb->tke->eur->nsid):
     if (res = NO_ERR) {
         nsid = ocb->tkc->cur->nsid:
    break;
case TK_TT_MSTRING:
    /* match all nodes in the namespace w/ specified prefix */
    if (lpeb-stke-seur-snaid) {
         res = check_gname_prefix(pcb,
                                     TK CUR MOD(peb-stke),
                                      TK_CUM_MODLEN(peb->tke),
                                     &peb->tke->eur->nsid);
     if (res = NO_ERR) {
         nsid = peb-otke-beur-basid;
         name = TK_CUK_VAL(peb-stke);
    break;
case TK_TT_TSTRING:
     /* check the ID token for a NodeType name */
     nodetyp = get_nodetype_id(TK_CUR_VAL(peb->tke));
     if (nodetyp = xr_exnt_NONe ||
         (tk_next_typ(peb->tke) i= TK_TT_LPAREN)) {
         name = TK_CUR_VAL(peb-stke);
         break;
    3
     /* get the node test left paren */
     res = xpath_parse_token(peb, TK_TT_LPAREN);
    if (res 1= NO_ERR) {
         return res;
     /* check if a literal param can be present */
     if (nodetyp - xp_exNT_PROC_INST) {
          (* check if a literal param is present */
         nexttyp = tk_next_typ(pcb->tkc);
if (nexttyp==rk_tr_QSTMING ||
             nexttyp TK_TT_SQSTRING)
              /* temp save the literal string */
              res = xpath_parse_token(peb, nexttyp);
             if (res 1= NO_ERR) {
                  return res;
        1
     /* get the node test right paren *
     res = xpath_parse_token(peb, TK_TT_RPAREN);
    if (res 1= NO_ERR) {
         return res:
   if (peb-sflags & XP_PL_INSTANCEID) {
        invalid_instanceid_error(pcb);
return ERR_NCX_INVALID_INSTANCEID;
    /* process the result based on the node type test */
   switch (nodetyp) {
   case XP_EXNT_COMMENT
        /* no connents to match */
        emptyresult = TRUE;
if (peb-pobl &&
            peb->logerrors &&
            nex_warning_enabled(ERR_NCX_EMPTY_XPATH_RESULT)) {
            log_warn("\nwarning: no comment nodes available in
"xpath expr "%s"",
                     peb-sexpratr);
             nex_print_errormsg(peb->tke,
                                peb->tkern.mod.
                                ERA_NCX_EMPTY_XPATH_RESULT);
        > else if (ocb-pobined != NULL) {
            nex_ine_warnings(peb-sobjmod);
        1
        break;
   case XP_EXNT_TEXT:
        /* match all leaf of leaf-list content */
emptyresult = FALSE;
        textnode = TRUE;
        break:
   case XP_EXNT_PROC_INST:
           no processing instructions to match */
        emptyresult - TRUE;
        SF (oeb-sob1 88
                                                                       2
            peb->logerrors &&
            per-ingerior and (enn_NCX_EMPTY_XPATH_RESULT)) {
    log_warn('\maximum_ncg: no processing instruction "
        "nodes available in "
        "xpath expr 'Xs'",
```

Example

- <u>http://en.wikipedia.or</u> g/wiki/Event driven f inite state machine
- Trivially simple
- Still has dependency on "application"

```
typedef enum {
                 RADIO, CD
          STATES;
          int readEventFromMessageQueue(void);
           int main (void)
            /* Default state is radio */
            int state = RADIO;
            stationNumber=0;
            trackNumber=0;
            /* Infinite loop */
            while(1)
             -{
              /* Read the next incoming event. Usually this is a blocking function
              event = readEventFromMessageQueue()
              /* Switch the state and the event to execute the right transition.
              switch( state )
               Ł
               case RADIO:
                 switch (event)
                   - E
                   case mode:
                     /* Change the state */
                    state = CD;
                    break:
                   case next:
                     /* Increase the station number */
                    stationNumber++;
                    break;
                   3
                 break:
                case CD:
                  switch (event)
                    -
                    case mode:
                      /* Change the state */
                      state = RADIO;
                     break;
                    case next:
                      /* Go to the next track */
                      trackNumber++;
                      break;
                    3
                  break:
Writing a Testa
```

The TCP State Machine



Linux TCP State Machine

- tcp_states.h
- State logic scattered over dozens of large files in /net
- State changes often 'incidental' in other code.
- Completely untestable

```
TCP_MAX_STATES
```

};

The Problem

- Not separating concerns: include code in SM that **implements** the state.
- Seemingly trivial, but introduces dependency(s) on the application
- Mixes state logic with application logic.
- Testing
 - Manual? Run application, stimulate it, observe outcome. Infer state machine operation
 - printf()
 - Stubbing & mocking to get a test suite to build \rightarrow tedious
 - Too hard \rightarrow pressure not to.

So ...

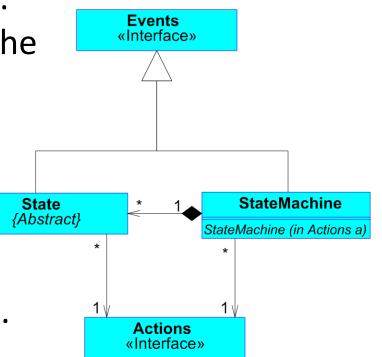
How should we go about writing a state machine from scratch so that we can test it easily?

What is a State Machine?

- "Code that manages the state of something, responding to external events, and translating them into actions to be implemented by the system" ©me.
- Transitions from one state to another in response to events.
- Transitions normally expected to cause actions, but aren't a requirement.
- Details of actions are NOT part of the state machine.

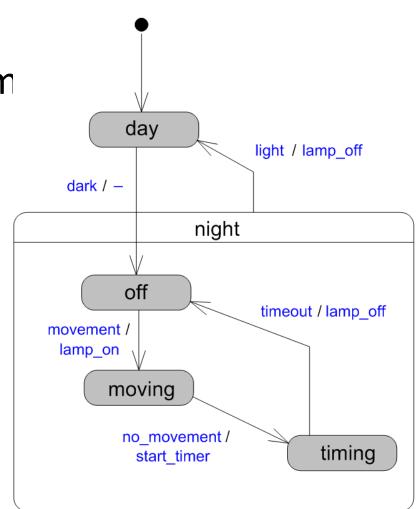
The Premise

- SM is simply a way of turning events into actions.
- Any further details should be in the application.
- Maps nicely to an OO approach:
- An *events* interface describing the events the SM will respond to.
- An actions interface describing the actions the SM will output to the system.
- SM implements events.
- Application implements actions.

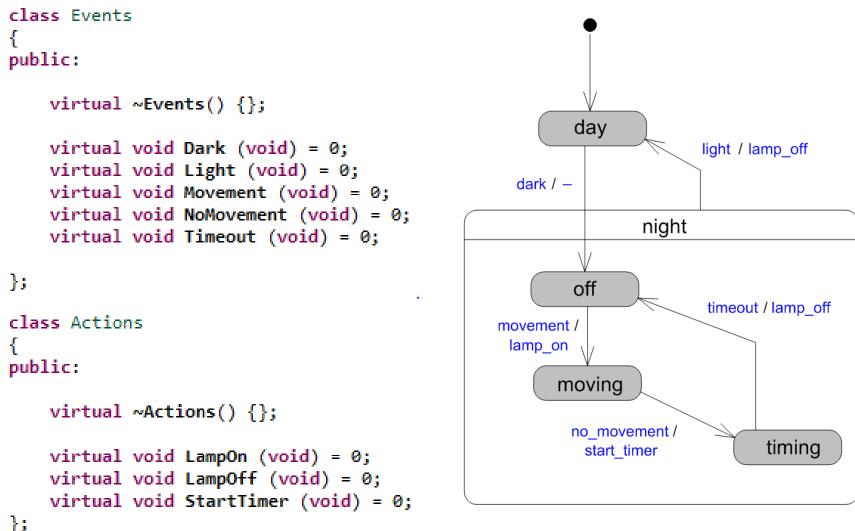


Making A Start

- Start from state diagram
- Identify inputs and outputs:
 - Inputs \rightarrow events
 - Outputs \rightarrow actions

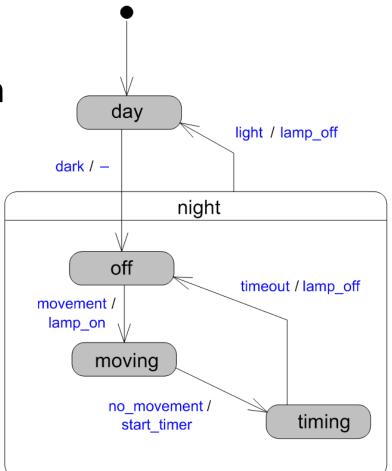


Events and Actions



Writing Some Tests

- Given the events we know the FSM can accept, we can write our first test to fire off an event and expect the action.
- Then another.
- And another.
- Soon we should have a test for every transition on the diagram.
- See worked example later



Writing Some Tests

- To add real world relevance to the work, add use case tests.
- May have implemented the diagram perfectly, but unless we put it through its paces it might not be apparent that the diagram is flawed.
- Mistake in example :-)

Approaches

- Language, application, company, project specific.
- Derive test actions from *actions* interface and inject (i.e. Test doubles)
- Derive test implementation of SM to allow test code to sense transitions and actions
- Mock the *actions* interface.
- If non-00, stub action functions

Method: Transition Tests

- Bootstrap the SM, and test harness, into existence:
- Write a few transition tests: look for some expected actions and resulting states
- Get code & test framework into place and settled
- Once the state machine is starting to grow, move to test vectors to simplify the tests, and move faster:
 - [starting state, event(s), end state, expected action(s)]

Example Test Vector

struct TestVector

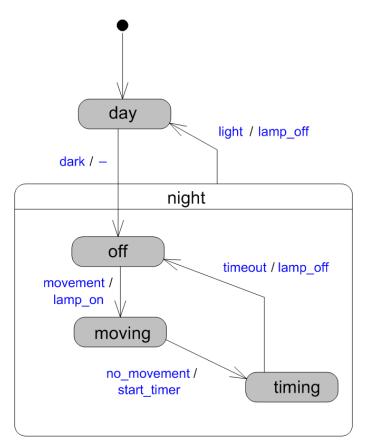
ſ

};

// Type for a pointer to void void member function of StateMachine.
typedef void (StateMachine::* SMFunctionPointer) (void);

const char * testTitle; StartingState startingState; SMFunctionPointer eventFunctionToApply;

const char * expectedState; const TestActions::ActionType *firstAction; const TestActions::ActionType *secondAction; const TestActions::ActionType *thirdAction;



Method: Use Case Tests

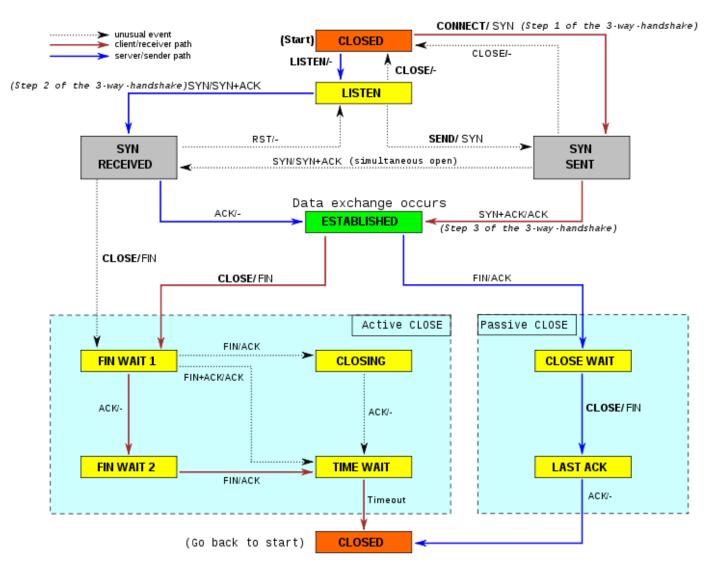
- Add use case tests: inject >1 events, driving the SM round multiple transitions.
- Use cases should be good & bad, realistic & unlikely.
- Use cases apply the SM to the real world application: they are acceptance tests (GOOS).
- Each failing acceptance test lead us to TDD the requisite transitions:
 - Add the necessary single transition test vectors, and code the missing transitions / actions.
- Rinse and repeat

Example Test Vectors

const TestVector v[] =

| 1 | | | | | |
|-----|---------------------------------------|------------------|---------------------------|-----------|-------------------------------------|
| ٠, | "Day + dark -> off", | START_IN_DAY, | StateMachine::Dark, | "Off", | 0, 0, 0 }, |
| - | "Day + light -> no change", | START IN DAY, | StateMachine::Light, | "Day", | 0, 0, 0 }, |
| _ | "Day + movement -> no change", | START IN DAY, | StateMachine::Movement, | "Day", | 0, 0, 0 }, |
| | "Day + no movement -> no change", | START_IN_DAY, | StateMachine::NoMovement, | | 0, 0, 0 }, |
| - | "Day + timeout -> no change", | START IN DAY, | StateMachine::Timeout, | - | 0, 0, 0 }, |
| ι | bay + cimeouc -> no change , | START_IN_DAT, | StatemachineTimeout, | "Day", | 0, 0, 0 <u>]</u> , |
| { | "Off + dark -> no change", | START IN OFF, | StateMachine::Dark, | "Off", | 0, 0, 0 }, |
| - | "Off + light -> lamp off; day", | START IN OFF, | StateMachine::Light, | "Day", | &LampOff, 0, 0 }, |
| | "Off + movement -> lamp on; moving", | | StateMachine::Movement, | | &LampOn, 0, 0 }, |
| | "Off + no movement -> no change", | START IN OFF, | StateMachine::NoMovement, | | 0, 0, 0 }, |
| - | "Off + timeout -> no change", | START IN OFF, | StateMachine::Timeout, | "Off", | 0, 0, 0 }, |
| , i | off f cimeode of no change ; | 5/////_1/_0///, | Statemachinerrineouty | , | 0, 0, 0], |
| { | "Moving + dark -> no change", | START_IN_MOVING, | StateMachine::Dark, | "Moving", | 0, 0, 0 }, |
| { | "Moving + light -> lamp off; day", | START IN MOVING, | StateMachine::Light, | "Day", | &LampOff, 0, 0 }, |
| _ | "Moving + movement -> no change", | START IN MOVING, | StateMachine::Movement, | | 0, 0, 0 }, |
| | "Moving + no_movement -> start timer; | ; timing", | | - | |
| - | | START IN MOVING, | StateMachine::NoMovement, | "Timing", | <pre>&StartTimer, 0, 0 },</pre> |
| { | "Moving + timeout -> no change", | START IN MOVING, | StateMachine::Timeout, | "Moving", | 0, 0, 0 }, |
| | | | - | | |
| { | "Timing + dark -> no change", | START_IN_TIMING, | StateMachine::Dark, | "Timing", | 0, 0, 0 }, |
| - í | "Timing + light -> lamp off; day", | START IN TIMING, | StateMachine::Light, | "Day", | &LampOff, 0, 0 }, |
| Ì | "Timing + movement -> moving", | | StateMachine::Movement, | | 0, 0, 0 }, |
| | "Timing + no movement -> no change", | | - | | <pre>&StartTimer, 0, 0 },</pre> |
| - | "Timing + timeout -> lamp off; off", | | | - | &LampOff, 0, 0 }, |
| };` | | | | | |
| | | | | | |

Code Examples



Events and Actions

```
class Events
Ł
public:
    virtual ~Events() {}
    virtual void connect() = 0;
    virtual void listen() = 0;
    virtual void close() = 0;
    virtual void send() = 0;
    virtual void rst() = 0;
    virtual void ack() = 0;
    virtual void syn() = 0;
    virtual void syn_ack() = 0;
    virtual void fin() = 0;
    virtual void fin ack() = 0;
    virtual void timeout() = 0;
};
```

```
class Actions
{
  public:
    virtual ~Actions() {}
    virtual void syn() = 0;
    virtual void syn_ack() = 0;
    virtual void ack() = 0;
    virtual void fin() = 0;
```

```
};
```

One State & Transition

```
class StateMachine : public Events
{
  public:
    Actions & actions;
```

```
StateMachine( Actions & a )
     : actions( a ) {}
```

```
virtual void connect() { actions.syn(); }
   virtual void listen() {}
   virtual void close() {}
   virtual void send() {}
   virtual void rst() {}
   virtual void ack() {}
   virtual void syn() {}
   virtual void syn ack() {}
   virtual void fin() {}
   virtual void fin ack() {}
   virtual void timeout() {}
};
class TestStateMachine : public StateMachine
public:
    TestStateMachine( Actions & a )
        : StateMachine( a ) {}
   const char * getState() { return "SYN SENT"; }
};
```

```
class MockActions : public Actions
public:
    MOCK METHOD0(syn, void());
    MOCK METHOD0(syn ack, void());
    MOCK METHOD0(ack, void());
    MOCK METHOD0(fin, void());
};
TEST( Transitions,
      In Closed Connect_goes_to_SYN_SENT )
{
    MockActions actions;
    TestStateMachine sm( actions );
    sm.connect();
    ASSERT_STREQ( "SYN SENT", sm.getState() );
TEST( Transitions,
      In Closed Connect does syn )
Ł
    MockActions actions;
    EXPECT CALL( actions, syn() );
    StateMachine sm( actions );
    sm.connect();
```

}

Two States & Transitions

```
enum TCPState
{
    CLOSED,
    SYN SENT,
    LISTEN
};
class StateMachine : public Events
ſ
public:
    Actions & actions;
    TCPState currentState:
    StateMachine( Actions & a )
    : actions( a ),
      currentState( CLOSED )
    {}
    virtual void connect()
        actions.syn();
        currentState = SYN SENT;
    }
    virtual void listen()
        currentState = LISTEN;
    virtual void close() {}
    virtual void send() {}
    // ...
```

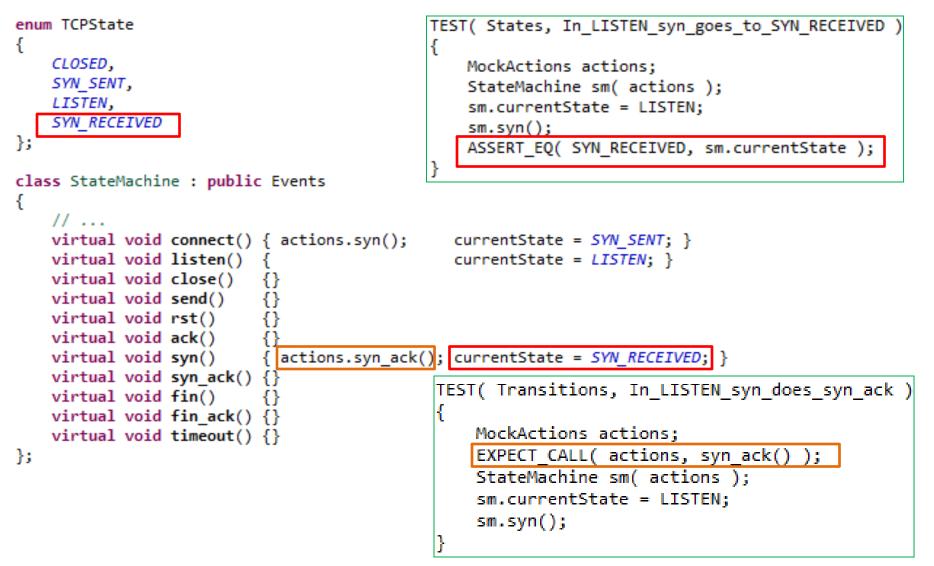
```
// Tests for end states
```

```
In CLOSED_connect_goes_to_SYN_SENT ) {
   MockActions actions;
    StateMachine sm( actions );
    sm.connect();
    ASSERT EQ( SYN SENT, sm.currentState );
TEST( Transitions,
      In CLOSED listen goes to LISTEN ) {
   MockActions actions:
    StateMachine sm( actions );
    sm listen().
    ASSERT EQ( LISTEN, sm.currentState );
```

```
// Tests for actions
```

```
TEST( Transitions,
      In CLOSED connect does syn ) {
   MockActions actions;
    EXPECT CALL( actions, syn() );
    StateMachine sm( actions );
    sm.connect();
}
TEST( Transitions,
      In CLOSED listen does nothing ) {
   MockActions actions;
    // expect no actions
    StateMachine sm( actions );
    sm.listen();
```

Add Third Transition & State



Introduce Test Vectors

```
struct TransitionTestVector
    typedef void (StateMachine::* EventMethod)();
    TCPState
                startingState;
    EventMethod eventToInject;
                expectedEndState;
    TCPState
};
void ExecuteOneTestVector( const TransitionTestVector& v
ł
    MockActions actions;
    StateMachine sm( actions );
    sm.currentState = v.startingState;
    (sm.*(v.eventToInject))();
    ASSERT EQ( v.expectedEndState, sm.currentState );
}
TEST( States, In CLOSED connect goes to SYN SENT )
ſ
    const TransitionTestVector v =
         CLOSED. &StateMachine::connect, SYN SENT };
    ExecuteOneTestVector( v );
}
TEST( States, In CLOSED listen goes to LISTEN )
ſ
    const TransitionTestVector v =
        { CLOSED, &StateMachine::listen, LISTEN };
    ExecuteOneTestVector( v );
}
                                    Writing a Testable State Machine
```

Add Actions To Test Vector

```
struct TransitionTestVector
```

{

ł

```
typedef void (StateMachine::* EventMethod)();
```

| | TCPState | <pre>startingState;</pre> |
|----|-------------|---------------------------|
| | EventMethod | eventToInject; |
| | TCPState | expectedEndState; |
| | int | expectedCallsToSyn; |
| | int | expectedCallsToSynAck; |
| | int | expectedCallsToAck; |
| | int | expectedCallsToFin; |
| }; | | |

void ExecuteOneTestVector(const TransitionTestVector& v)

```
MockActions actions:
```

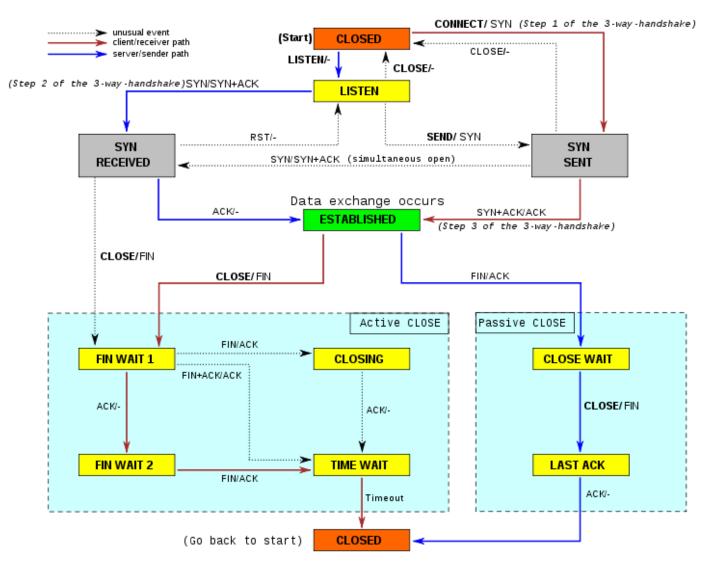
| EXPECT_CALL(actions | , syn() | <pre>).Times(v.expectedCallsToSyn);</pre> |
|----------------------|-------------|---|
| EXPECT_CALL(actions | , syn_ack() |).Times(v.expectedCallsToSynAck); |
| EXPECT_CALL(actions | , ack() | <pre>).Times(v.expectedCallsToAck);</pre> |
| EXPECT_CALL(actions | , fin() | <pre>).Times(v.expectedCallsToFin);</pre> |

```
StateMachine sm( actions );
sm.currentState = v.startingState;
(sm.*(v.eventToInject))();
ASSERT_EQ( v.expectedEndState, sm.currentState );
}
TEST( States, In_CLOSED_connect_goes_to_SYN_SENT )
{
const TransitionTestVector v = { CLOSED, &StateMachine::connect, SYN_SENT, 1, 0, 0, 0 };
ExecuteOneTestVector( v );
}
```

Merge Existing Tests Into One

```
TEST( States, Test all state to state transitions and resulting actions )
ł
   const int SYN = 1:
   const int S A = 1;
   const TransitionTestVector v[] =
   Ł
       { CLOSED, &StateMachine::connect, SYN SENT, SYN, 0, 0, 0 },
       { CLOSED, &StateMachine::listen, LISTEN, 0, 0, 0, 0},
       { LISTEN, &StateMachine::syn, SYN RECEIVED, 0, S A, 0, 0 }
   };
   for( int i = 0; v[i].expectedCallsToFin != 99; ++i )
       ExecuteOneTestVector( v[i] );
```

(Reminder)



Add More Transitions And States

StateMachine(Actions & a) : actions(a), currentState(CLOSED) {}

```
virtual void connect() { actions.syn();
                                           currentState = SYN SENT; }
virtual void listen()
                                           currentState = LISTEN; }
virtual void close()
                                           currentState = CLOSED; }
virtual void send()
                        actions.syn();
                                           currentState = SYN SENT; }
virtual void rst()
                                           currentState = LISTEN; }
virtual void ack()
                                           currentState = ESTABLISHED; }
virtual void syn()
                        actions.syn ack(); currentState = SYN RECEIVED; }
                        actions.ack();
virtual void syn ack() {
                                           currentState = ESTABLISHED; }
virtual void fin()
                       {}
                            const TransitionTestVector v[] =
virtual void fin ack() {}
virtual void timeout() {}
                            {
                                { CLOSED,
                                                &StateMachine::connect, SYN SENT,
                                                                                     SYN, 0,
                                                                                                    0 },
                                                                                               0,
                                { CLOSED,
                                                &StateMachine::listen, LISTEN,
                                                                                     0, 0,
                                                                                               0,
                                                                                                    0 },
                                { LISTEN,
                                                &StateMachine::syn,
                                                                       SYN RECEIVED, 0,
                                                                                                    0 },
                                                                                          S A, Ø,
                                                &StateMachine::send,
                                { LISTEN,
                                                                       SYN SENT,
                                                                                     SYN, 0,
                                                                                               0,
                                                                                                    0},
                                                &StateMachine::close,
                                                                       CLOSED,
                                { LISTEN,
                                                                                          0.
                                                                                     0.
                                                                                               0.
                                                                                                    0},
                                { SYN SENT,
                                                &StateMachine::close,
                                                                       CLOSED,
                                                                                     0,
                                                                                          0, 0,
                                                                                                    0 },
                                                &StateMachine::syn,
                                { SYN SENT,
                                                                                          S A, Ø,
                                                                       SYN RECEIVED, 0,
                                                                                                    0},
                                                &StateMachine::syn ack, ESTABLISHED, 0,
                                { SYN SENT,
                                                                                               ACK, 0 },
                                                                                          0,
                                { SYN RECEIVED, &StateMachine::rst,
                                                                       LISTEN,
                                                                                     0.
                                                                                          0.
                                                                                               0.
                                                                                                    0 },
                                { SYN_RECEIVED, &StateMachine::ack,
                                                                       ESTABLISHED, 0,
                                                                                          0.
                                                                                               0.
                                                                                                    0 },
                                { CLOSED, &StateMachine::connect, CLOSED, 0, 0, 0, 99 }
                            };
```

| <pre>virtual void send() { if(currentState != CLOSED) { actions.syn(); currentState = SYN_SENT; } }</pre> | | Add Self Transitions For Closed | | | | | |
|---|--------------------------------|---|---|--|----------------------------------|--|--|
| <pre>virtual void rst() { if(currentStat</pre> | te != CLOSED) te = LISTEN; | | | | | | |
| <pre>} } virtual void ack() { if(currentStat</pre> | { CLOSED, | <pre>TestVector v[] = &StateMachine::connect, &StateMachine::listen, &StateMachine::close, &StateMachine::send, &StateMachine::rst, &StateMachine::ack, &StateMachine::syn, &StateMachine::syn, &StateMachine::fin, &StateMachine::fin, &StateMachine::fin_ack, &StateMachine::timeout,</pre> | LISTEN, CLOSED, CLOSED, CLOSED, CLOSED, CLOSED, CLOSED, CLOSED, CLOSED, | SYN, 0, 0, 0, 0, 0, 0, 0, | 0, 0, 0, 0, 0, 0, | 0, 0, 0, 0, 0, 0, 0, | 0 }, 0 }, 0 }, 0 }, 0 }, 0 }, 0 }, 0 }, |

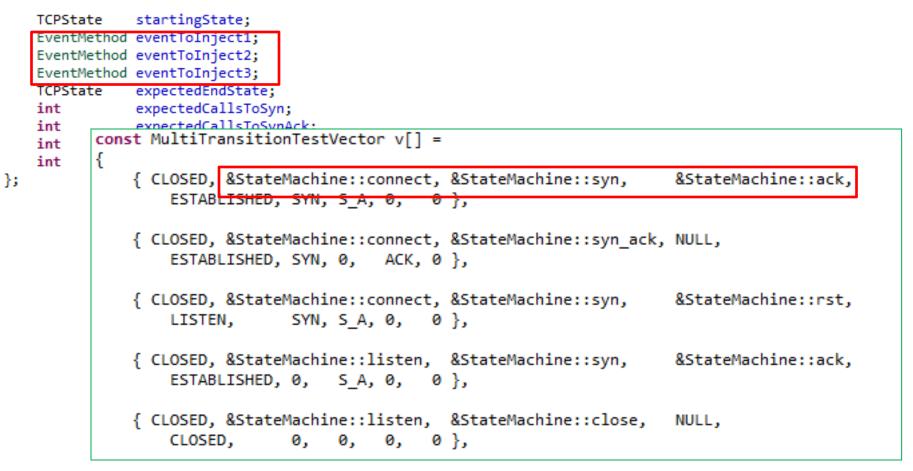
Add Some Use Case Tests

struct MultiTransitionTestVector

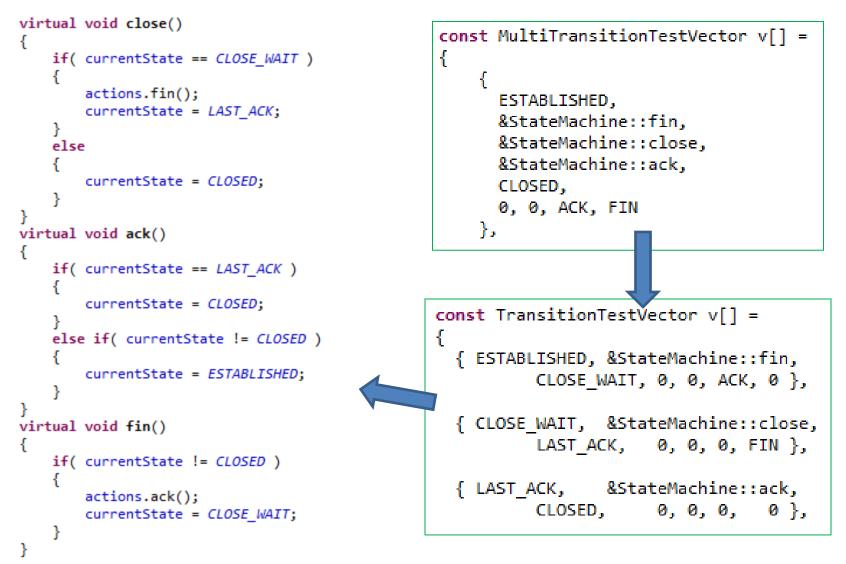
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typedef void (StateMachine::* EventMethod)();

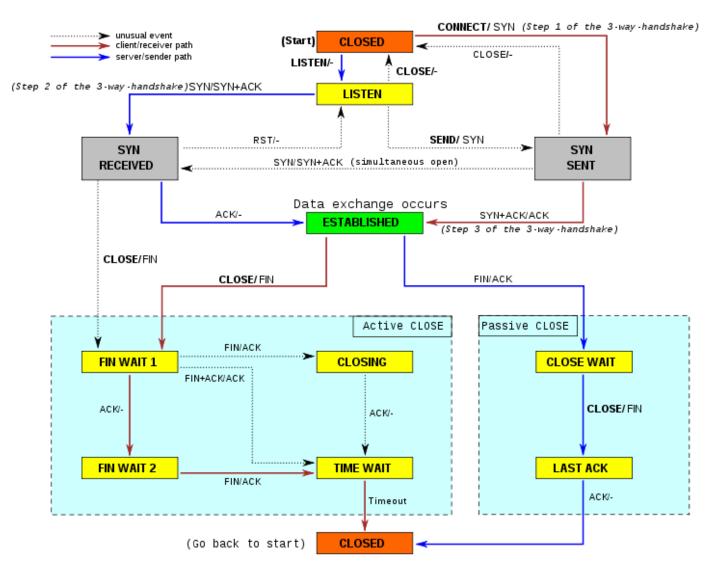
No changes to SM code



Add Use Case for ESTABLISHED \rightarrow CLOSED



Finished Code



Conclusions

- If we are growing the code in response to the tests, what does it look like?
 - Who cares? It works.
 - TDD: it should be pretty simple
 - GoF OO state machine pattern is unlikely to occur spontaneously.
 - The same goes for hierarchical states, even if shown on the state diagram.
 - But once tests are in place, we are free to refactor to this if we want.

Conclusions

- Maintain separation of concerns (app/SM)
- Test for every event in every state to prevent surprises & prove completeness
- Jump between acceptance tests (use case) and unit tests (transitions, actions)
- Use case tests can drive design in absence of state diagram
- Given a state transition diagram we can test the SM into existence and prove it completely

Questions ?

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